

30001 U.S. PTO  
10/043763

01/09/2002

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM

10043763

FILING DATE

01/09/2002

CLASS

327

SUBCLASS

534

GAU

2816  
2956

EXAMINER

Cunningham

APPLICANTS:

Ajit Janardhanan;

\*\*CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/260,580 01/09/2001

AND CLAIMS BENEFIT OF 60/260,582 01/09/2001

TDC

\*\* FOREIGN APPLICATIONS VERIFIED:

NONE  
TDC

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☐ yes ☒ no

35 USC 119 conditions met

☐ yes ☒ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

41980/RJP/B600

TITLE : Sub-micron high input voltage tolerant input output (I/O) circuit which accommodates large power supply variations

U.S. DEPT. OF COMM/PAT. & TM-PTO-43EL (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

Assistant Examiner

CLAIMS ALLOWED

Total Claims

Print Claim for  
O.G.

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheets Drawn

Figs. Drawn

Print Fig.

Primary Examiner

PREPARED FOR ISSUE

Application Examiner



TERMINAL

DISCLAIMER

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☒ CD-ROM

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